

Notice of Allowability	Application No.	Applicant(s)	
	10/810,676	AHN ET AL.	
	Examiner Toan Ton	Art Unit 2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to papers filed 12/18/06.
2. The allowed claim(s) is/are 1-19.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows: nonelected claims 20-38 without traverse have been canceled.

The abstract has been combined into one paragraph as follows:

ABSTRACT

A liquid crystal display using horizontal electric field and a method of fabricating the liquid crystal display device that are capable of reducing the number of mask processes are provided. The liquid crystal display of horizontal electric field applying type has a thin film transistor array substrate, with a conductive film connected to a gate pad, a data pad and a common pad of a thin film transistor on the substrate. The pads are exposed on the thin film transistor array substrate.

REASONS FOR ALLOWANCE

2. Claims 1-19 are allowed.

The following is an examiner's statement of reasons for allowance: the prior art of record does not anticipate nor render obvious to one ordinary skilled in the art a liquid crystal display of horizontal electric field applying type (in-plane-switching) comprising a combination of various elements as claimed, more specifically, at least the following combinations: a pixel electrode connected to the thin film transistor and formed to produce horizontal electric field along with the common electrode in the pixel area; a gate pad formed with at least one conductive layer included in the gate line; a data pad formed with at least one conductive layer included in the data line; a common pad formed with at least one conductive layer included in the common line; a passivation film exposing the gate pad, the data pad and the common pad, which are formed on a substrate to form the thin film transistor array substrate; a color filter array combined with the thin film transistor array substrate, liquid crystal material being filled between the color filter and the thin film transistor array substrate; and a conductive film directly connected to the gate pad, the data pad and the common pad, the pads exposed on the thin film transistor array substrate.

CONTACT INFORMATION

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

February 23, 2007

TOANTON
PRIMARY PATENT EXAMINER

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connected to the gate pad, the data pad and the common pad, the pads exposed on the thin film transistor array substrate.

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